

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Toshio YAMADA

Serial No.: 09/779,751

Filed: February 09, 2001



Group Art Unit: 2154

Examiner:

For: SEMICONDUCTOR INTEGRATED CIRCUIT, COMPUTER SYSTEM, DATA PROCESSOR AND DATA PROCESSING METHOD

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Supplemental Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Each additional reference was cited in a corresponding foreign application search report or office action and its relevance discussed therein.

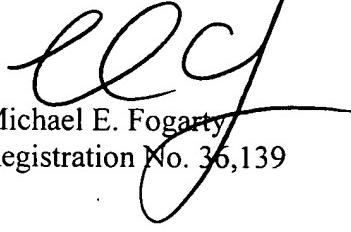
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A copy of the foreign search report or office action, is attached for the Examiner's information.

Respectfully submitted,

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